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October 15, 2003

Customer No. 23911

Mail Stop Patent Application
Director of the United States
Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Re: New U.S. Patent Appln. Our Ref: 056203.52845US

Sir:

Transmitted herewith for filing is the patent application of:

## Koki ISHIZAKI and Takeshi SHIRATO

entitled: THE MULTILAYER ELECTRONIC SUBSTRATE, AND THE METHOD OF MANUFACTURING MULTILAYER ELECTRONIC SUBSTRATE

## Enclosed are:

- 1. Specification, including  $\underline{15}$  claims and Abstract of the Disclosure ( $\underline{46}$  pages, and  $\overline{\text{title page}}$ ).
- 2.  $\frac{8}{4}$  Sheets of Formal drawings showing Figs. 1-2, 3A-3F, 4A- $\frac{4}{4}$ G, and 5-8.
- 3. Preliminary Amendment.
- 4. Information Disclosure Statement & Form PTO-1449 with 1 reference.

Priority is being claimed under 35 U.S.C. §119 and 37 C.F.R. §1.55 based on Priority Document 2002-302229, filed in <u>Japan</u> on <u>October 16, 2002</u>.

The filing fee has been calculated as shown below:

The filing fee is being deferred.

espectfylly submitted

Richard R. Diefendorf

Reg. No. 32,39/0

RRD:msy